

AMENDMENT TRANSMITTAL LETTER (Large Entity)

Applicant(s): Victor W.C. Chan, et al.

Docket No.

FIS920030105US1 (16462)

Application No. 10/604,003	Filing Date 6/20/2003	Examiner Fazli Erdem	Customer No. 23389	Group Art Unit 2826	Confirmation No. 1002
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 Invention: **SUBSTRATE ENGINEERING FOR OPTIMUM CMOS DEVICE PERFORMANCE**

COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	7 -	20 =	0	x \$50.00	\$0.00
INDEP. CLAIMS	1 -	3 =	0	x \$200.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00

- No additional fee is required for amendment.
- Please charge Deposit Account No. _____ in the amount of _____
- A check in the amount of _____ to cover the filing fee is enclosed.
- The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 09-0458/IBM
 - Any additional filing fees required under 37 C.F.R. 1.16.
 - Any patent application processing fees under 37 CFR 1.17.
- Payment by credit card. Form PTO-2038.

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

Dated: March 21, 2005

 Leslie S. Szivos, Ph.D.
 Registration No. 39,394

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

03/21/05

(Date)

Signature of Person Mailing Correspondence

Leslie S. Szivos, Ph.D.

Typed or Printed Name of Person Mailing Correspondence

CC:

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Invention: SUBSTRATE ENGINEERING FOR OPTIMUM CMOS DEVICE PERFORMANCE



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TOTAL CLAIMS	7 -	20 =	0	x \$50.00	\$0.00
INDEP. CLAIMS	1 -	3 =	0	x \$200.00	\$0.00
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Victor W.C. Chan, et al.

Examiner: Fazli Erdem

Serial No: 10/604,003

Art Unit: 2826

Filed: June 20, 2003

Docket: FIS920030105US1 (16462)

Dated: March 21, 2005

For: SUBSTRATE ENGINEERING FOR OPTIMUM CMOS DEVICE
PERFORMANCE

Confirmation No.: 1002

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE

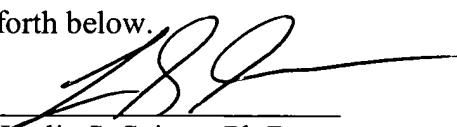
Sir:

In response to the Office Action dated December 20, 2004, applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

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Dated: March 21, 2005


Leslie S. Szivos, Ph.D.